



VePAL TX130M+

DSn/PDH and Ethernet Test Set for Legacy and Synchronized Packet Networks

VeEX™ VePAL TX130M+ is a full-featured Mobile Backhaul test solution supporting legacy PDH/DSn, Carrier Ethernet technologies, and Synchronized Packet networks.

Platform Highlights

- Intuitive presentation of measurements with test graphics
- High resolution color touch-screen viewable in any lighting conditions fitted with protective cover
- Robust, handheld chassis packed with powerful and flexible features for demanding environments and test conditions
- Ethernet port and connection for back office applications, workforce management and triple play service verification
- User defined test profiles and thresholds enable fast, efficient and consistent turn-up of services
- USB memory stick support and FTP upload capability for test result storage and file transfer respectively
- Maintain instrument software, manage test configurations, process measurement results and generate customer test reports using included ReVeal™ PC software
- Extend field testing time using interchangeable Lilon battery pack/s
- Perform remote testing and monitoring using the remote control option via standard Ethernet interface

Ethernet Features

- Supports 10/100/1000Base-T Copper Ethernet interface
- Supports 1000Base-X & 100FX Optical Ethernet interface
- Supports BERT, Throughput, Loopback, RFC2544

SyncE/IEEE 1588v2

- Fully integrated solution for synchronized packet networks
- Supports IEEE 1588v2/PTP and SyncE/ITU-T G.8261 standards
- Master Clock and Slave clock emulation
- IEEE 1588v2/PTP protocol monitoring and decoding
- IEEE 1588v2/PTP PDV analysis
- Clock recovery and translation from SyncE or IEEE 1588v2/PTP to E1 or DS1 port
- Wander measurement of SyncE clock
- Dual PDH/DSn & Ethernet testing with synchronized clocking

PDH/DSn Features

- Supports DS1, DS3, E1, E2, and E3 bit rates
- Dual Rx BERT on DS1, DS3 and E1 ports
- Full rate DS1, E1 & fractional Nx56 kbps or Nx64 kbps
- Non intrusive Pulse Mask Analysis
- Bit Error and Performance Analysis per ITU/Bellcore standards
- Histogram and Event Analysis for errors and alarms
- VF drop/insert via headset
- VF tone generation and measurement
- ISDN PRI (ANSI and ETSI) call set up and analysis
- Jitter Measurement
- Wander Measurement on E1 & SyncE recovered clock
- Transmit Frequency Offset to stress clock recovery circuits